

On Page 10, line 16, before "312", insert --data--;

On Page 11, line 21, delete "the address in packet vector";

On Page 11, line 22, before "the next" insert "--a pointer to"--;

On Page 11, line 22, delete "DRAM 300 of the first location";

On Page 12, line 24, delete "port" and substitute --packet pointer--;

On Page 12, line 28, after "next dequeue" insert --packet--;

On Page 15, line 8, delete "4040" and substitute --404--;

On Page 16, line 6, after second occurrence of "packet" insert --pointer--;

On Page 16, line 18, delete first occurrence of "enable" and substitute --entry--;

On Page 17, line 5, delete "dump";

On Page 17, line 5, after "by the" insert --dump--;

On Page 19, line 16, delete "and or 304";

On Page 19, line 18, after "packet" insert --pointer--;

On Page 19, line 25, delete "(Fig. 7)";

On Page 19, line 25, after "714" insert --(Fig. 7)--;

On Page 21, line 5, delete "706 (Fig. 7)" and substitute --entry 306 (Fig. 3)--;

On Page 21, line 15, delete "dequeued" and substitute --dequeue--; and

On Page 21, line 15, delete second occurrence of "300" and substitute --310--.

Please amend claims as follows:

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- A1 12. (Amended) A queue as claimed in Claim 1 [ or Claim 11] wherein a packet vector stored in the second memory includes a link to a next packet vector in the queue.
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- A2 20. (Amended) A queuing method as claimed in Claim 18 [or Claim 19] wherein the cache row is transferred in a single write cycle.
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- A3 24. (Amended) A queuing method as claimed in Claim 13 [or 23] wherein a packet vector stored in the second memory includes a link to a next packet vector.
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- A4 37. (Amended) A queue as claimed in Claim 25 [or 36] wherein a packet vector stored in the second memory includes a link to a next packet vector in the queue.
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